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## APPLICANTS

Tadashi Yamaguchi, Tokyo, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

NONE

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 138584/2003 05/16/2003

VERIFIED

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	9	6	1
Examiner's Signature <i>TEMB</i> Initials <i>TA</i>				

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## TITLE

Semiconductor device and method for fabricating semiconductor device

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RECEIVED 770		<input type="checkbox"/> 1.16 Fees ( Filing )
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